

A2
On this switch instruction has a first mode for carrying out the conversion processing by said first DA converter and a second mode for carrying out the conversion processing by said second DA converter, and wherein said memory with small capacity corresponds to said first DA converter, and the memory with large capacity corresponds to said second DA converter.

19. (Amended) A image display apparatus according to either one of claims 13 and 14, wherein said display unit changes the number of the independent display pixels of said display unit according to the instruction from said control, and displays according to said analog image signal.

20. (Amended) A image display apparatus according to any one of claims 12 to 14, wherein said first DA converter outputs an analog image signal with binary gradation.

21. (Amended) A image display apparatus according to any one of claims 12 to 14, further comprising an illumination means for supplying light to said display unit, wherein the illumination means supplies light to said display unit in said second mode.

IN THE ABSTRACT OF THE DISCLOSURE:

Please replace the original abstract with the attached abstract.